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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/680,126	10/03/2000	John Newlin	083818 0269851	6144

27498 7590 08/10/2004

PILLSBURY WINTHROP LLP  
2475 HANOVER STREET  
PALO ALTO, CA 94304-1114

EXAMINER
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STEELMAN, MARY J

ART UNIT	PAPER NUMBER
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2122

DATE MAILED: 08/10/2004

10

Please find below and/or attached an Office communication concerning this application or proceeding.

**Advisory Action**

Application No.

09/680,126

Applicant(s)

NEWLIN ET AL.

Examiner

Mary J. Steelman

Art Unit

2122

**--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --**

THE REPLY FILED 03 March 2004 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

**PERIOD FOR REPLY [check either a) or b)]**

- a) ☐ The period for reply expires \_\_\_\_\_ months from the mailing date of the final rejection.
- b) ☒ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection. **ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).**

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

1. ☐ A Notice of Appeal was filed on \_\_\_\_\_. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.
2. ☒ The proposed amendment(s) will not be entered because:
- (a) ☒ they raise new issues that would require further consideration and/or search (see NOTE below);
  - (b) ☐ they raise the issue of new matter (see Note below);
  - (c) ☐ they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
  - (d) ☐ they present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: See Continuation Sheet.

3. ☐ Applicant's reply has overcome the following rejection(s): \_\_\_\_\_.
4. ☐ Newly proposed or amended claim(s) \_\_\_\_\_ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
5. ☐ The a) ☐ affidavit, b) ☐ exhibit, or c) ☐ request for reconsideration has been considered but does NOT place the application in condition for allowance because: \_\_\_\_\_.
6. ☐ The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.
7. ☒ For purposes of Appeal, the proposed amendment(s) a) ☒ will not be entered or b) ☐ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: \_\_\_\_\_.

Claim(s) objected to: \_\_\_\_\_.

Claim(s) rejected: 1-20.

Claim(s) withdrawn from consideration: \_\_\_\_\_.

8. ☐ The drawing correction filed on \_\_\_\_\_ is a) ☐ approved or b) ☐ disapproved by the Examiner.
9. ☐ Note the attached Information Disclosure Statement(s) (PTO-1449) Paper No(s). \_\_\_\_\_.
10. ☐ Other: \_\_\_\_\_

Continuation of 2. NOTE: The amendments to independent claim 9: "...automatically generating a hardware description of a configurable processor...automatically generating information..." and independent claim 12, "...means responsive to the determining means for directing a state-accessing instruction stream to an appropriate one of the multiple cores..." will require further search and/or consideration.

Regarding the arguments on page 6:

(A) "Duboc does not describe or suggest transmitting anything from a debugger to an interpreting agent, as required by claims 1 and 13.

Examiner's Response: Claim 1 recites "transmitting, using a debugger..." It does not recite, "...from a debugger..." FIG. 3 shows (col. 4 lines 1-4) various software components utilized to perform debugging with an enhanced programmable core, including a debug gui, interpreter, debug monitor, core model....

(B) Duboc fails to suggest "transmitting a state-accessing instruction stream to an interpreting agent, which is then used to return processor state information to the debugging application as clearly required by claims 1 and 13."

Examiner's Response: Col. 3, lines 44-46, "...a completely integrated simulation and debug environment (state) may be provided to a user..." Col. 5, lines 6-18, "A debug operation may include...register display operations to display the states of one or more internal registers in a core model..." See FIG. 3. An instruction stream is transmitted. Processor state information is provided.

Regarding the arguments on page 7:

(C) Duboc fails to disclose "including actual processor instructions in a debug GUI script" and "instructions that are executed by the processor to access state", as required by independent claims 1 and 13.

Examiner's Response: These are not claim limitations of claims 1 or 13.

(D) "Duboc does not disclose or suggest anything about configurable processors, much less generating a hardware description of a configurable processor based on a user description."

Examiner's Response: Generating a hardware description of a configurable processor based on a user description is not a claim limitation. See col. 3, lines 33-34, "enhanced programmable core model..."

(E) "Duboc does not disclose or suggest anything about save and restore instructions for state as specified in claim 9.

Examiner's Response: See FIG. 10. Col. 14, line 53-col. 15, line 6, "To develop an enhanced processor core...Design of an enhanced core design begins at block 252, and is based upon the specification of a processor core, as illustrated at 252. From the processor specification...a number of details about the processor core are collected...the debug monitor and script is based upon customizing and/or reusing (to use saved code scripts to restore when developing) generic library program code.

Regarding the arguments on page 8:

(F) Duboc does not disclose or suggest anything about save and restore state instructions that can be executed by an embedded processor to access state information, much less a debugger library that can generate such instruction streams based on a user specification of a configurable processor as required by claim 10."

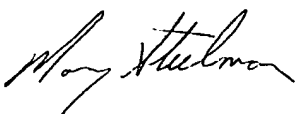
Examiner's Response: Col. 15, lines 1-4, "...script is based upon customizing and/or reusing (saved code to use for a restore) generic library program code, to reduce the amount of effort required to create core-specific debug functionality." Access state instructions – col. 15, line 36, "a register window display script..."

Regarding the arguments on page 9:

(G) Duboc does not disclose "directing a state-accessing instruction stream to an appropriate one of the multiple cores in response to the determination.

Examiner's Response: This is not a claim limitation of claim 12.

Examiner maintains the rejections of claims 1-20.



**TUAN DAM**  
**SUPERVISORY PATENT EXAMINER**